

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	568	257/211.ccls.	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/26 13:34
L2	342	257/210.ccls.	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/26 14:17
L3	649	(layout with structure) and cell and ((p-channel and n-channel) or (complementary adj FETs))	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/26 13:55
L4	432	3 and (multiplexer or inverter)	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/26 13:54
L5	224	4 and wiring	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/26 13:46
L6	54	5 and (wiring or layout).ab.	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/26 13:56
L7	21	6 and transfer	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/26 13:48
L8	12	("5311476").URPN.	USPAT	OR	ON	2004/12/26 13:53
L9	432	3 and (multiplexer or inverter or (multiplexer-inverter))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/26 13:56
L10	900	(layout with structure) and cell and ((p-channel and n-channel) or (complementary adj FETs))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/26 14:08
L11	596	10 and (multiplexer or inverter or (multiplexer-inverter))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/26 14:08

L12	230	11 and (wiring or layout or structure).ab.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/26 14:08
L13	96	12 and transfer	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/26 14:09
L14	38	13 and (transfer adj gate)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/26 13:58
L15	4	14 and (Hidaka).in.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/26 13:58
L16	15	("20020000872"   "20020079542"   "3806741"   "4672243"   "5512771"   "5610533"   "5880604"   "5909140"   "5942784"   "5985706"   "6133762"   "6255698"   "6307234"   "6333541"   "6529042").PN.	US-PGPUB; USPAT; USOCR	OR	ON	2004/12/26 13:59
L17	10	("4475050"   "4672243"   "5789788"   "5847432"   "5900665"   "5929667"   "5985706"   "6093947").PN. OR ("6307234").URPN.	US-PGPUB; USPAT; USOCR	OR	ON	2004/12/26 14:01
L18	1885	(layout with structure) and cell and ((p-channel and n-channel) or (complementary adj FETs) or CMOS)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/26 14:08
L19	1057	18 and (multiplexer or inverter or (multiplexer-inverter))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/26 14:08
L20	403	19 and (wiring or layout or structure).ab.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/26 14:08

L21	173	20 and transfer	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/26 14:11
L22	18	21 and ((2-layer or multiple or multi) with wiring)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/26 14:11
L23	3	("5635737"   "5995734"   "6335640").PN.	US-PGPUB; USPAT; USOCR	OR	ON	2004/12/26 14:15
L24	369	257/210.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/26 14:30
L25	650	257/211.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/26 14:40
L26	1920	257/369.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/26 14:46
L27	627	257/371.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/26 15:09
L28	503	257/206.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/26 15:34
L29	934	257/208.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/26 15:34